Multi-tap Digital Canceller for Full-Duplex Applications

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Abstract—We identify phase noise as a bottleneck for the performance of digital self-interference cancellers that utilize a single auxiliary receiver—single-tap digital cancellers—and operate in multipath propagation environments. Our analysis demonstrates that the degradation due to phase noise is caused by a mismatch between the analog delay of the auxiliary receiver and the different delays of the multipath components of the self-interference signal. We propose a novel multi-tap digital self-interference canceller architecture that is based on multiple auxiliary receivers and a customized Least-Mean-Squared (LMS) filtering for self-interference regeneration. Our simulation results demonstrate that our proposed architecture is more robust to phase noise impairments and can in some cases achieve 10 dB larger self-interference cancellation than the single-tap architecture.

I. INTRODUCTION

Since wireless radios share a common propagation medium for transmitting and receiving data, they were up to now limited to a half-duplex mode of operation. In full-duplex mode, the proximity of the transmit and receive antenna generates a high powered self-interference signal at the receiver side, which shadows the intended signal from a remote transmitter. Therefore, in a single time-frequency resource, the radio could either receive or transmit data, but not simultaneously. This separation is inefficient since it basically halves the performance of the system as a whole. A full-duplex wireless radio on the other hand can recover this performance loss, provided it would be able to handle the self-interference signal. Recent advances in transceiver design have enabled this application at practical costs, and managed to restrict the self-interference below the noise floor at the receiver side [1], [2].

However, it has been shown that self-interference cancellers are very sensitive to imperfections in the transceiver components [3]–[5]. Different architectures to deal with such imperfections have been proposed, each with its own advantages and limitations. One recently proposed architecture for digital self-interference cancellation utilizes an auxiliary receiver that *taps* a copy of the analog self-interference signal before the transmitter antenna. This single-tap digital canceller architecture has been described in [5]–[10]. One key advantage of this solution is that no estimation nor compensation of transmitter-side impairements is required; they basically have no effect in the performance of the system since the auxiliary receiver *knows* the impaired self-interference signal and does not try to decode it [5]. However, the receiver-side impairments can still degrade the performance.

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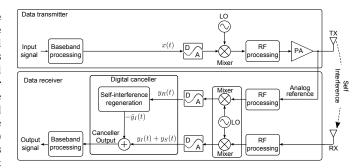


Fig. 1. Full-duplex transceiver using a single-tap digital self-interference canceller.

In this paper, we identify that the performance of the auxiliary receiver architecture of [5]-[10] degrades in the presence of phase noise as a function of the delay spread of the channel—thereby confirming the observations of [5] where channels with shorter coherence bandwidth exhibited worse performance. In contrast to [5], our results stem from a time domain analysis that applies to single or multicarrier systems, while the results in [5] focused on the analysis of a multicarrier scenario. We show that performance degradation due phase noise is fundamentally a problem of delay between the auxiliary receiver channel and the self-interference channel. We provide an analysis of the degradation induced by the phase noise process; these results show similarities to the effect of oscillator phase noise on analog full-duplex cancellers [4]. We then introduce in Section III a new architecture that uses possibly many analog reference taps in the auxiliary receiver. We use these multiples views on the self-interference signal to design simple least-mean square (LMS) adaptive filters, which automatically adapt to spread channels without having to estimate the phase noise process. These filters are reminiscent of approaches used in echo cancellation when the acoustic channel has very large delays [11]. As simulation results will demonstrate, our proposed multi-tap canceller can achieve much larger self-interference cancellation than the single-tap one and is more robust to increasing delay spread.

II. PHASE NOISE EFFECT ON THE SINGLE-TAP DIGITAL CANCELLER

In this section we analyze the phase noise effect on the full-duplex transceiver shown in Fig.1. It implements time domain cancellation as in [8], [9] through a single analog reference tapped from the transmit signal. The architecture consists in one transmitter chain with a digital to analog converter (D/A), a mixer fed by a local oscillator (LO) and RF processing that includes conventional filtering stages followed

by a power amplifier (PA) whose output signal feeds the transmitter antenna. On the receiver side, there is one receiver chain connected to the receiver antenna and one receiver chain connected to the tapped analog reference. Each receiver chain consists of an analog to digital converter (A/D), a mixer fed by the LO (which is shared by all receiver mixers as in [5], [8]) and RF processing which includes filtering stages and a low noise amplifier (LNA)—which may not be required by the auxiliary receiver [8]. We assume there is no analog cancellation as in [5]; receiver saturation is avoided via the use of passive techniques [12]. This architecture is thus more suited for scenarios with either low transmit power or high passive suppression. At the receiver, the signals at the output of the A/D converters are input to the time domain digital self-interference cancellation, which consists in subtracting an estimate of the self-interference signal from the signal coming from the receiver antenna. In this paper we consider a selfinterference regeneration that is implemented via an adaptive filtering stage. This approach of using an adaptive filter has been briefly mentioned in [9] but its performance was not evaluated. Note that we assume a calibration period where the equalizer is trained without the presence of the signal of interest $y_S(t)$. A calibration period for estimation of the self-interference signal has been used in related work [1], [8], where the authors have shown that it benefits the overall system in terms of net interference cancellation. The penalty of this calibration period is obviously a reduction in the rate [9].

We now analyze the residual self-interference for the full-duplex transceiver in Fig.1, with an emphasis on the effect of the phase noise at the receiver and the relative delay between the reference and the self-interference. Let the self-interference channel be a single path channel with attenuation h_I and delay Δ_I . The received self-interference signal, including the effect of receiver phase noise $\phi(t)$, is equal to

$$y_I(t) = h_I e^{j\phi(t)} x(t - \Delta_I). \tag{1}$$

Let h_R and Δ_R denote the attenuation and delay of the analog reference signal path. The reference signal at the receiver is equal to

$$y_R(t) = h_R e^{j\phi(t)} x(t - \Delta_R). \tag{2}$$

Let us define $h_{IR}=h_I/h_R$ and $\Delta_{IR}=\Delta_I-\Delta_R$. We can write the received self-interference as a function of the reference signal as follows

$$y_I(t) = h_{IR}e^{j(\phi(t) - \phi(t - \Delta_{IR}))}y_R(t - \Delta_{IR}). \tag{3}$$

From the above equation we see that, due to the phase noise term $e^{\jmath(\phi(t)-\phi(t-\Delta_{IR}))}$, the relation between $y_I(t)$ and $y_R(t)$ changes with each time sample. This fast variation is hard to track for the adaptive filter that performs the self-interference regeneration. Adaptive algorithms used in equalization will converge over time to the *average* channel between the input and output. The average channel depends on the stochastic process $\phi(t)-\phi(t-\Delta_{IR})$. Most commonly, $\phi(t)$ is modeled as Gaussian processes—e.g. Wiener processes or Ornstein-Uhlenbeck processes [5], [13]. The distribution of $\phi(t)$ for all t is then a Gaussian random variable with some variance that may depend on time [14]. The difference process is thus

also a Gaussian process, and usually has zero expectation; under mild conditions there is no trend in the mean and $\mathbb{E}[\phi(t)] = \mathbb{E}[\phi(t-\Delta_{IR})]$. Assume then that the difference process is distributed as $\theta(t) = \phi(t) - \phi(t-\Delta_{IR}) \sim \mathcal{N}(0, \sigma_{\epsilon}^2(t))$. Through the characteristic function of a Gaussian random variable, we have that

$$\mathbb{E}\left[e^{j\theta(t)}\right] = \exp\left(-\frac{\sigma_{\epsilon}^2(t)}{2}\right) = K(t). \tag{4}$$

The reconstructed interference will then be

$$\widehat{y}_I(t) = h_{IR}K(t)y_R(t - \Delta_{IR}). \tag{5}$$

Using $\widehat{y}_I(t)$ in Eq. (5) to cancel the self-interference $y_I(t)$ will leave some residual interference after cancellation which is computed as

$$r(t) = (y_I(t) + w_I(t)) - (\hat{y}_I(t) + w_R(t)), \tag{6}$$

where $w_I(t)$ represents AWGN in the receiver of the self-interference signal and $w_R(t)$ represents residual error of the self-interference regeneration that is due to AWGN in the reference and self-interference signals. By replacing (5) and (2) in (6) and doing some basic simplifications we obtain

$$r(t) = \left(e^{j\phi(t)} - K(t)e^{j\phi(t-\Delta_{IR})}\right)h_I x(t-\Delta_I) + w_I(t) - w_R(t), \tag{7}$$

The power of the residual self-interference is once again dependent on the statistics of the stochastic process $\theta(t)$. Let $N_I = \mathbb{E}\left[\left|w_I(t)\right|^2\right]$ and $N_R = \mathbb{E}\left[\left|w_R(t)\right|^2\right]$ be the noise powers, and let $N = N_I + N_R$. Furthermore, let $P_I = \left|h_I\right|^2 \mathbb{E}\left[\left|x(t)\right|^2\right]$ be the self-interference power. We can write the power as

$$E\left[\left|r(t)\right|^{2}\right] = P_{I}\mathbb{E}\left[\left|e^{\jmath\phi(t)} - K(t)e^{\jmath\phi(t-\Delta_{IR})}\right|^{2}\right] + N$$

$$= P_{I}\left(1 + K(t)^{2} - K(t)\left(\mathbb{E}\left[e^{\jmath\theta(t)}\right] + \mathbb{E}\left[e^{-\jmath\theta(t)}\right]\right)\right) + N$$

$$= P_{I}(1 - K(t)^{2}) + N \tag{8}$$

Notice that the residual interference due to phase noise reduces to zero when $\Delta_I = \Delta_R$ for which $K(t)^2 = 1$ regardless of the phase process; the detrimental effect of phase noise only manifests itself when $\Delta_I \neq \Delta_R$. This observation implies that if the self-interference channel consists of only one path, then the delay Δ_R of the auxiliary receiver should be set as close as possible to the delay of the self-interference path. However, achieving this is not possible when the self-interference channel is a multipath channel. In such scenario it is not possible to match all the path delays by using a single auxiliary receiver: there will be a residual self-interference that stems from multipath components for which $\Delta_I \neq \Delta_R$.

The above observations are verified in Fig. 2. The simulation results are obtained from a simulation of the transceiver in Fig. 1 and an OFDM signal x(t). The details of the simulation parameters are presented in the appendix. Fig. 2 shows results for the case of a single path self interference channel with $\Delta_R=0$. We observe the residual self-interference power

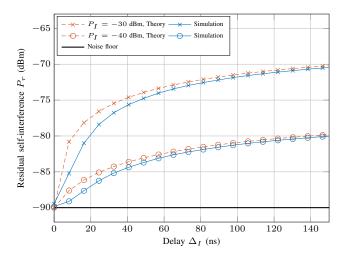


Fig. 2. Residual interference power achieved by the single-tap digital self-interference canceller for the scenario of a single path channel with a varying path delay Δ_I .

 P_r increases as $\Delta_I - \Delta_R$ increases and results verify the closed form expression derived in (8). The difference between simulations and our theoretical result can stem from the fact that, for simplicity, our analysis did not include the effect of pulse shaping and upsampling at the transmitter and matched filtering and downsampling at the receiver. We did include these effects in the simulator, as explained in the appendix.

Fig. 3 shows results for the case of a channel with two paths, where the first past has path loss $10\log_{10}|h_I^{(1)}|^2=-50$ dB and a delay $\Delta_I^{(1)}=0$ ns and the second path has a path loss of $10\log_{10}|h_I^{(2)}|^2=-60$ dB and delay $\Delta_I^{(2)}$ that varies between 0 and 150 ns, this choice of values is based on the time response channel measurements reported in [12]. We show results for different values of the reference signal delay Δ_R . The results in Fig. 3 show that the performance of the digital canceller degrades—and the residual power increases—as the delay spread $\delta_S=\Delta_I^{(2)}-\Delta_I^{(1)}$ for this multipath channel

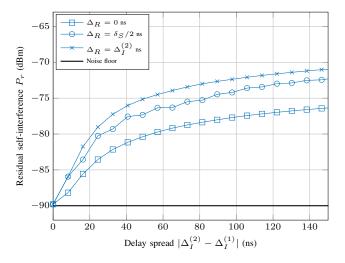


Fig. 3. Residual interference power achieved by the single-tap digital self-interference canceller for the scenario of a 2-path channel with with absolute delay spread $|\Delta_I^{(2)}-\Delta_I^{(1)}|.$

increases. In other words, in the presence of phase noise, the performance of the digital canceller degrades as the delay spread of the channel increases and this is true for any value of Δ_R . The choice of $\Delta_R=0$ is what works best because this matches the delay of the strongest self-interference path. The choice of $\Delta_R=\Delta_I^{(2)}$ matches the delay of the weakest path, thus the strongest path is not properly matched and results in higher residual. The choice of $\Delta_R=\delta_S/2$ matches better the strongest path hence it is better than when $\Delta_R=\Delta_I^{(2)}$.

III. THE MULTI-TAP CANCELLER

Overall, using a single tap can effectively eliminate the residual due to phase noise whenever the self-interference channel is a single path channel and this can be achieved by setting $\Delta_R=\Delta_I.$ However, due to phase noise at the receiver, the single tap architecture will not be able to reduce the self-interference to the noise floor when the self-interference channel consists on multiple paths. The key insight in our improved self-interference canceller is that if we were to have 2 reference signals with different delays such that $\Delta_R^{(1)}=\Delta_I^{(1)}$ and $\Delta_R^{(2)}=\Delta_I^{(2)},$ then we could choose to match each path in the channel with the corresponding reference and avoid the degradation due to phase noise.

A. Self-interference regeneration using multiple references

We generalize this architecture in Fig. 4 and propose the use of N reference signal branches. Note that the delays of each branch need to be radio-frequency components, since they have to occur before the downconversion of the signal. Note also that the branch connected to reference signal n, with delay $\Delta_R^{(n)}$, can only be used to regenerate self-interference components that arrive with a delay larger than $\Delta_R^{(n)}$. Consequently we apply a digital delay $\Delta_I' \geq \Delta_R^{(N)}$ to the received self-interference signal to guarantee that all the filter branches are useful. Using $h_R^{(n)}$ and $\Delta_R^{(n)}$ to denote the attenuation and delay of the n-th analog reference signal, we write this signal as

$$y_R^{(n)}(t) = h_R^{(n)} e^{j\phi(t)} x(t - \Delta_R^{(n)}).$$
 (9)

Assume that the self-interference signal consists of M multipath components, and let $t'=t-\Delta_I'$ be the relative time

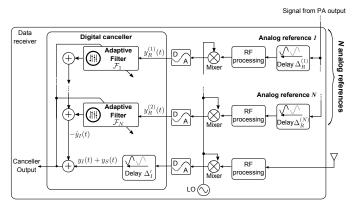


Fig. 4. Proposed multi-tap digital self-interference canceller.

at the receiver. The observed self-interference after the digital delay Δ_I' is given by

$$y_I(t) = \sum_{m=1}^{M} h_I^{(m)} e^{j\phi(t')} x(t' - \Delta_I^{(m)}), \tag{10}$$

where $h_I^{(m)}$ and $\Delta_I^{(m)}$ denote respectively the attenuation and delay that the self-interference experiences over path m. Let us define $h_{IR}^{(m,n)} = h_I^{(m)}/h_R^{(n)}$ and $\Delta_{IR}^{(m,n)} = \Delta_I^{(m)} - \Delta_R^{(n)}$. The difference phase process is then $\theta^{(m,n)}(t') = \phi(t') - \phi\left(t' - \Delta_{IR}^{(m,n)}\right)$. In a generalization of (3), we can write the received self-interference using the n-th reference as follows

$$y_I(t) = \sum_{m=1}^{M} h_{IR}^{(m,n)} e^{j\theta^{(m,n)}(t')} y_R^{(n)}(t' - \Delta_{IR}^{(m,n)}).$$
 (11)

As before, the farthest the delay of the reference path $\Delta_R^{(n)}$ from the delay of the multipath component $\Delta_I^{(m)}$, the more degraded the reconstruction will be in average. A direct solution to would be to average the reconstructed signals from the various references, in an effort to reduce the variance of the noise in the reconstruction [11]. However, tradeoffs have to be made between the adaptive filter lengths and the number of references as to avoid noise enhancement effects. An alternative solution is to ensure that only the references whose $\Delta_R^{(n)}$ is closest to the delay $\Delta_I^{(m)}$ of the m-th path are chosen to reconstruct this specific path. We now detail this second solution.

B. An example of self-interference regeneration based on LMS and using multiple references

Consider the filter \mathcal{F}_n with L taps. This filter has as an input the analog reference with delay $\Delta_R^{(n)}$. Let T_s be the sample period of the input to the filter and thus the time separation between the filter taps. Due to the digital delay Δ_I the filter \mathcal{F}_n will put the largest weight to tap $\left[\Delta_I'/T_s\right]$ when regenerating a self-interference signal with $\Delta_I^{(m)} = \Delta_R^{(n)}$. In other words, tap number $\lceil \Delta_I'/T_s \rceil$ of filter \mathcal{F}_n mainly contributes to regenerate the part of the self-interference signal that arrives with delay closest to $\Delta_R^{(n)}$. Consequently, for each filter \mathcal{F}_n the tap number $\left[\Delta_I^{\prime}/T_s\right]$ and neighboring taps should be given larger weights so that they are larger contributors for the regeneration of the self-interference signal arriving with delay close to $\Delta_R^{(n)}$. This weighting can be performed via a multiplicative factor, which we denote as $p^{(n)}$ that scales either the filter weights or the filter input of the reference n. We have found via simulations that scaling the filter input results in faster convergence. Following the above insight, we propose a customized LMS algorithm as explained below.

Let $t_k = kT_s$ be the sample times for $k \in \mathbb{Z}$. Consider an architecture as in Fig. 4 and assume $\Delta_R^{(n)} = (n-1)T'$ for $n=1,\ldots,N$; consecutive references therefore have a delay difference of T'. Assume $T' \geq T_s$. Define $\boldsymbol{y}_R^{(n)}(t_k)$ as the vector representing the last L samples received from reference n

$$\boldsymbol{y}_{R}^{(n)}(t_{k}) = [y_{R}^{(n)}(t_{k}), y_{R}^{(n)}(t_{k} - T_{s}), ..., y_{R}^{(n)}(t_{k} - (L-1)T_{s})]$$

Algorithm 1 Multitap self-interference canceller algorithm

Input: Step size μ .

Input: Received signal $y_I(t_k)$.

Input: Reference signals $\boldsymbol{y}_R^{(n)}(t_k)$ for all $1 \leq n \leq N$.

(A) Compute mask for filter input.

Initialize $[\boldsymbol{p}^{(n)}]_l = 0$, for $1 \leq l \leq L$ and $1 \leq n \leq N$.

Compute $\delta_1 = \lceil \frac{\Delta'_I}{T_s} \rceil$ and $\delta_2 = \lceil \frac{T'}{(2T_s)} \rceil$.

for $1 \leq n \leq N$ do

if $(T' > 2T_s)$ then $[\boldsymbol{p}^{(n)}]_l = 1$ for $\delta_1 - \delta_2 \leq l \leq \delta_1 + \delta_2$ elsif $(T' = 2T_s)$ then $[\boldsymbol{p}^{(n)}]_l = 1$ for $\delta_1 \leq l \leq \delta_1 + 1$

end for

(B) Run adaptive algorithm.

else $[\boldsymbol{p}^{(n)}]_l = 1$ for $l = \delta_1$

Initialize $g(t_k) \Leftarrow 0$

for all
$$k \in \mathbb{N}$$
 do
$$\mathbf{Y}(t_k) \Leftarrow \begin{bmatrix} \mathbf{y}_R^{(1)}(t_k) \odot \mathbf{p}^{(1)}, \dots, \mathbf{y}_R^{(N)}(t_k) \odot \mathbf{p}^{(N)} \end{bmatrix}$$
 $\mathbf{u}(t_k) \Leftarrow \mathbf{vec}\left(\mathbf{Y}(t_k)\right)$ $\widehat{y}_I(t_k) \Leftarrow \mathbf{g}^H(t_k)\mathbf{u}(t_k)$ $e(t_k) \Leftarrow y_I(t_k) - \widehat{y}_I(t_k)$ $g(t_{k+1}) \Leftarrow \mathbf{g}(t_k) - \mu \frac{e(t_k)^* \mathbf{u}(t_k)}{\mathbf{u}(t_k)^H \mathbf{u}(t_k)}$ end for

Output: Estimated self-interference $\widehat{y}_I(t_k)$ for all $k \in \mathbb{N}$.

The *j*-th entry of $\boldsymbol{y}_{R}^{(n)}(t_{k})$, which we denote as $[\boldsymbol{y}_{R}^{(n)}(t_{k})]_{j}$, is given by

$$\left[\boldsymbol{y}_{R}^{(n)}(t_{k}) \right]_{j} = h_{R}^{(n)} e^{\jmath \phi(t_{k} - (j-1)T_{s})} x \left(t_{k} - (j-1)T_{s} - \Delta_{R}^{(n)} \right)$$

Let us define $u(t_k)$ as the vector with NL elements that is input to the bank of adaptive filters at time t_k . Furthermore, we define $g(t_k)$, with NL elements, as the vector representing the values of the filter taps for the N filters at time t_k . Our proposed LMS-like adaptive filter approach for self-interference regeneration based on multiple references is shown in Alg. 1. We use \odot as the Hadamard entrywise product between matrices or vectors, and $\mathbf{vec}(\mathbf{A})$ as the vectorize operation that stacks the columns of matrix \mathbf{A} in a column vector. We note that our proposed construction of $\mathbf{p}^{(n)}$ in Alg. 1 is based on a heuristic approach derived from our earlier observations. Obtaining the optimal $\mathbf{p}^{(n)}$ is currently part of our future work.

Simulation results shown in Fig. 5 compare the baseline single-tap canceller explained in Section II versus our propose multi-tap canceller for a single path self-interference channel. The results demonstrate the improvement thanks to our proposed multi-tap approach. We observe that when the delay of the self-interference channel Δ_I matches one of the reference delays $\Delta_R^{(n)}$, the residual interference falls back to the noise floor. Fig. 6 shows results for a channel with two paths as was assumed and described for the simulations of Fig. 3. Notice the performance improvement thanks to the use of multiple references. With four taps, our design is almost reducing the self-interference to the noise floor for delay spreads up to 100 ns—the delay of the last reference. The performance degrades beyond this value.

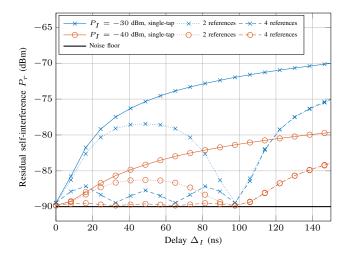


Fig. 5. Comparison of the residual interference power achieved by the single-tap canceller versus the residual achieved by the proposed multi-tap canceller for the scenario of a single path channel with a varying path delay Δ_I . All reference taps are spaced equally between 0 and 100 ns.

Finally, we point out that all the filters evaluated in our simulations have a total of 32 taps. For our design in Alg. 1 the vector $g(t_k)$ has NL elements. However, we can simplify the filter and reduce the total equalizer length to only L elements since the mask p on the input signal has many zero values. The details of this simplification are not included due to lack of space, and will be detailed in an upcoming work.

IV. CONCLUSION

In this paper we have presented a new architecture for digital self-interference cancellation that is based on a novel use of multiple reference signals and a new customized LMS filtering for self-interference regeneration. This architecture achieves larger levels of cancellation than state-of-the-art single-tap solutions which, as we have demonstrated, can only match the delay of a single path channel hence their performance degrades in the presence of phase noise for multi path channels with non zero delay spread.

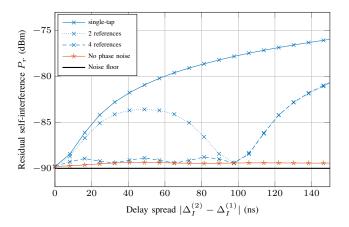


Fig. 6. Comparison of the residual interference power achieved by the single-tap canceller versus the residual achieved by the proposed multi-tap canceller for the scenario of a 2-path path channel with absolute delay spread $|\Delta_I^{(2)} - \Delta_I^{(1)}|$. All reference taps are spaced equally between 0 and 100 ns.

APPENDIX

Our simulations correspond to an OFDM signal with 20MHz bandwidth. We use an FFT size of 2048, sampling frequency of 122.88 MHz for the A/D and D/A converters and a sampling frequency of 30.72 MHz before (after) upsampling at the transmitter (downsampling at the receiver) which corresponds to an upsampling (downsampling) factor of 4. We model independent phase noise processes at the transmitter and receiver and consider the MAX2829 oscillator used in the WARP boards, which have been used in several demonstrations of full-duplex systems [1], [4], [15]. We model the oscillator as a Wiener process; in this case the difference process $\phi(t) - \phi(t - \Delta)$ is distributed as $\mathcal{N}(0, \sigma_{\epsilon}^2 |\Delta|)$. We compute σ_{ϵ}^2 from the PSD provided in the datasheet [16] and its value is set to 8.5095e-5, in units of squared radians. In our simulations, the phase noise is incorporated at sampling frequency of 122.88 MHz and the self-interference regeneration and cancellation occurs on the signals running at 30.72 MHz. The noise floor is equal to -90 dBm in all simulations.

REFERENCES

- M. Duarte, C. Dick, and A. Sabharwal, "Experiment-driven characterization of full-duplex wireless systems," *IEEE Trans. Wireless Commun.*, vol. 11, no. 12, pp. 4296–4307, December 2012.
- [2] D. Korpi, J. Tamminen, M. Turunen, T. Huusari, Y.-S. Choi, L. Anttila, S. Talwar, and M. Valkama, "Full-duplex mobile device: pushing the limits," *IEEE Commun. Mag.*, vol. 54, no. 9, pp. 80–87, September 2016.
- [3] T. Riihonen, P. Mathecken, and R. Wichman, "Effect of oscillator phase noise and processing delay in full-duplex OFDM repeaters," in *Asilomar Conf. Sig. Sys. Comp. (ASILOMAR)*, nov 2012.
- [4] A. Sahai, G. Patel, C. Dick, and A. Sabharwal, "On the impact of phase noise on active cancelation in wireless full-duplex," *IEEE Trans. Veh. Technol.*, vol. 62, no. 9, pp. 4494–4510, nov 2013.
- [5] E. Ahmed and A. M. Eltawil, "All-digital self-interference cancellation technique for full-duplex systems," *IEEE Trans. Wireless Commun.*, vol. 14, no. 7, pp. 3519–3532, jul 2015.
- [6] E. Ahmed and A. Eltawil, "Full-duplex self-interference cancellation systems," Patent US:2015:0318 976, 2015.
- [7] J.-I. Choi, M. Jain, and J. Mehlman, "Systems and methods for phase noise mitigation," Patent US:2015:0049834, 2015.
- [8] D. Korpi, L. Anttila, and M. Valkama, "Reference receiver based digital self-interference cancellation in MIMO full-duplex transceivers," in IEEE Glob. Commun. Conf. Workshops (GLOBECOM), 2014.
- [9] —, "Impact of received signal on self-interference channel estimation and achievable rates in in-band full-duplex transceivers," in *Asilomar Conf. Sig. Sys. Comp. (ASILOMAR)*, 2014.
- [10] S. Li and R. D. Murch, "Full-duplex wireless communication using transmitter output based echo cancellation," in *IEEE Global Telecom*munications Conference (GLOBECOM), 2011.
- [11] R. Sharma, A. Chaturvedi, and G. Sharma, "Acoustic echo cancellation using multiple sub-filters," in *Conf. Convergent Tech. Asia-Pacific Region* (TENCON), 2003.
- [12] E. Everett, A. Sahai, and A. Sabharwal, "Passive self-interference suppression for full-duplex infrastructure nodes," *IEEE Trans. Wireless Commun.*, vol. 13, no. 2, pp. 680–694, February 2014.
- [13] S. Herbert, I. Wassell, T. H. Loh, and J. Rigelsford, "Characterizing the spectral properties and time variation of the in-vehicle wireless communication channel," *IEEE Trans. Commun.*, vol. 62, no. 7, pp. 2390–2399, July 2014.
- [14] J. M. Steele, Stochastic Calculus and Financial Applications. Springer, 2001.
- [15] D. Bharadia, E. McMilin, and S. Katti, "Full duplex radios," in ACM Sig. Commun. Conf. (SIGCOMM), 2013.
- [16] MAX2828 Oscillator Data Sheet. [Online]. Available: https://datasheets.maximintegrated.com/en/ds/MAX2828-MAX2829.pdf